# Schottky barrier diode RB095B-40

## Applications

General rectification (Common cathode dual chip)

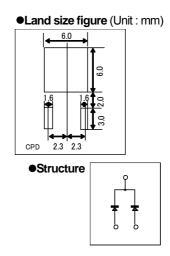
### Features

- 1) Power mold (CPD3)
- 2) High reliability
- 3) Low V<sub>F</sub> & Low I<sub>R</sub>

### **●**Construction

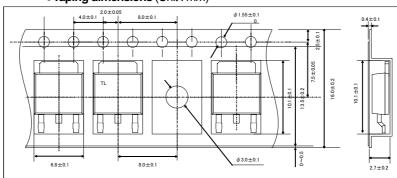
Silicon epitaxial planar

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## ●Taping dimensions (Unit: mm)

① Manufacture Date



### ● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Reverse voltage (repetitive peak)	$V_{RM}$	45	V
Reverse voltage (DC)	$V_R$	40	V
Average rectified forward current (*1)	lo	6	Α
Forward current surge peak (60Hz-1cyc) (*1)	I <sub>FSM</sub>	45	Α
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-40 to +150	°C

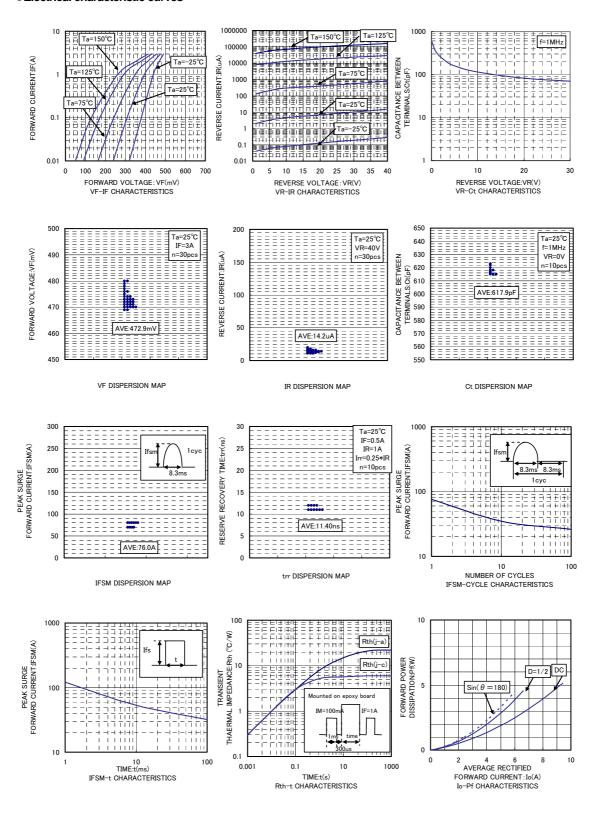
<sup>(\*1)</sup> Business frequencies, Rating of R-load, 1/2 lo per diode, Tc=120°C

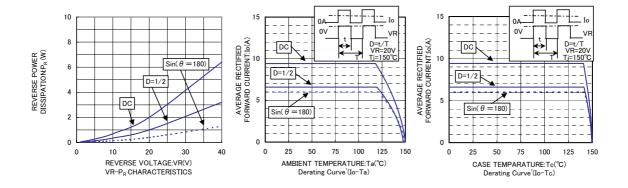
### ●Electrical characteristic (Ta=25°C)

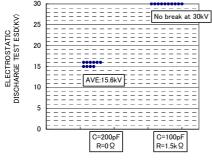
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	$V_{F}$	-	-	0.55	V	I <sub>F</sub> =3.0A
Reverse current	I <sub>R</sub>	-	-	100	μA	V <sub>R</sub> =40V
Thermal impedance	θјс	-	-	6.0	°C/W	junction to case

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### •Electrical characteristic curves







ESD DISPERSION MAP

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